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New circuit boards can be repeatedly recycled



Researchers have developed a new PCB that performs on par with traditional materials and can be recycled repeatedly with negligible material loss. Researchers used a solvent that transforms a type of vitrimer -- a cutting-edge class of polymer -- into a jelly-like substance without damage, allowing solid components to be plucked out for reuse or recycling. With these 'vPCBs' (vitrimer printed circuit boards), researchers recovered 98% of the vitrimer and 100% of the glass fiber.

A recent United Nations report found that the world generated 137 billion pounds of electronic waste in 2022, an 82% increase from 2010. Yet less than a quarter of 2022's e-waste was recycled. While many things impede a sustainable afterlife for electronics, one is that we don't have systems at scale to recycle the printed circuit boards (PCBs) found in nearly all electronic devices.

PCBs -- which house and interconnect chips, transistors and other components -- typically consist of layers of thin glass fiber sheets coated in hard plastic and laminated together with copper. That plastic can't easily be separated from the glass, so PCBs often pile up in landfills, where their chemicals can seep into the environment. Or they're burned to extract their electronics' valuable metals like gold and copper. This burning, often undertaken in developing nations, is wasteful and can be toxic -- especially for those doing the work without proper protections.

A team led by researchers at the University of Washington developed a new PCB that performs on par with traditional materials and can be recycled repeatedly with negligible material loss.

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Researchers used a solvent that transforms a type of vitrimer -- a cutting-edge class of sustainable polymers -- to a jelly-like substance without damaging it, allowing the solid components to be plucked out for reuse or recycling. The vitrimer jelly can then be repeatedly used to make new, high-quality PCBs, unlike conventional plastics that degrade significantly with each recycling. With these "vPCBs" (vitrimer printed circuit boards), researchers recovered 98% of the vitrimer and 100% of the glass fiber, as well as 91% of the solvent used for recycling.

The researchers published their findings April 26 in Nature Sustainability. "PCBs make up a pretty large fraction of the mass and volume of electronic waste," said co-senior author Vikram lyer, a UW assistant professor in the Paul G. Allen School of Computer Science & Engineering. "They're constructed to be fireproof and chemical-proof, which is great in terms of making them very robust. But that also makes them basically impossible to recycle. Here, we created a new material formulation that has the electrical properties comparable to conventional PCBs as well as a process to recycle them repeatedly." Vitrimers are a class of polymers first developed in 2015. When exposed to certain conditions, such as heat above a specific temperature, their molecules can rearrange and form new bonds. This makes them both "healable" (a bent PCB could be straightened, for instance) and highly recyclable. "On a molecular level, polymers are kind of like spaghetti noodles, which wrap and get compacted," said co-senior author Aniruddh Vashisth, a UW assistant professor in the mechanical engineering department. "But vitrimers are distinct because the molecules that make up each noodle can unlink and relink. It's almost like each piece of spaghetti is made of small Legos."

The team's process to create the vPCB deviated only slightly from those used for PCBs. Conventionally, semi-cured PCB layers are held in cool, dry conditions where they have a limited shelf life before they're laminated in a heat press. Because vitrimers can form new bonds, researchers laminated fully cured vPCB layers. The researchers found that to recycle the vPCBs they could immerse the material in an organic solvent that has a relatively low boiling point. This swelled the vPCB's plastic without damaging the glass sheets and electronic components, letting the researchers extract these for reuse.

This process allows for several paths to more sustainable, circular PCB lifecycles. Damaged circuit boards, such those with cracks or warping, can in some cases be repaired. If they aren't repaired, they can be separated from their electronic components. Those components can then be recycled or reused, while the vitrimer and glass fibers can get recycled into new vPCBs.

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The team tested its vPCB for strength and electrical properties, and found that it performed comparable to the most common PCB material (FR-4). Vashisth and co-author Bichlien H. Nguyen, a principal researcher at Microsoft Research and an affiliate assistant professor in the Allen School, are now using artificial intelligence to explore new vitrimer formulations for different uses. Producing vPCBs wouldn't entail major changes to manufacturing processes. "The nice thing is that a lot of industries – such as aerospace, automotive and even electronics – already have processing set up for the sorts of two-part epoxies that we use here," said lead author Zhihan Zhang, a UW doctoral student in the Allen School. The team analyzed the environmental impact and found recycled vPCBs could entail a 48% reduction in global warming potential and an 81% reduction in carcinogenic emissions compared to traditional PCBs. While this work presents a technology solution, the team notes that a significant hurdle to recycling vPCBs at scale would be creating systems and incentives to gather ewaste so it can be recycled. "For real implementation of these systems, there needs to be cost parity and strong governmental regulations in place," said Nguyen. "Moving forward, we need to design and optimize materials with sustainability metrics as a first principle." Additional co-authors include Agni K. Biswal, a UW postdoctoral scholar in the mechanical engineering department; Ankush Nandi, a UW doctoral student in the mechanical engineering department; Kali Frost, a senior applied scientist at Microsoft Research; Jake A. Smith, a senior researcher at Microsoft Research and an affiliate researcher in the Allen School; and Shwetak Patel, a UW professor in the Allen School and the electrical and computer engineering department. This research is funded by the Microsoft Climate Research Initiative, an Amazon Research Award and the Google Research Scholar Program. Zhang was supported by the UW Clean Energy Institute Graduate Fellowship.

> Science Daily April 26, 2024 Source: University of Washington



Training and Placement Cell (Campus Placement)

Sr. No.	Name of Student	Company Name	Package
1.	Aayusg Bachhav	Force Motors	3.24
2.	Ishwari Bhausaheb Chechare	Force Motors	3.24
3.	Mohit Babulal Bagul	Force Motors	3.24
4.	Renuka Sanjay Adke	Force Motors	3.24
5.	Sushant Dhanaji Shelar	Force Motors	3.24
6.	Sweekruti Kishor Shetty	Force Motors	3.24
7.	Akshada Anil Ghaduse	Mahindra & Mahindra	4.2
8.	Ankush Hemant Joshi	Mahindra & Mahindra	4.2
9.	Aryan Dattatray Phopse	Mahindra & Mahindra	4.2
10.	Darshana Namdeo Borse	Mahindra & Mahindra	4.2
11.	Devang Sandip Birari	Mahindra & Mahindra	4.2

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Training and Placement Cell (Campus Placement)

Sr. No.	Name of Student	Company Name	Package
12.	Dipak Prabhakar Patil	Mahindra & Mahindra	4.2
13.	Isha Dhananjay Pawar	Mahindra & Mahindra	4.2
14.	Manodnya Bhalchandra Suryawanshi	Mahindra & Mahindra	4.2
15.	Prachi Sharad Sawant	Mahindra & Mahindra	4.2
16.	Shashikant Dhansing Lohar	Mahindra & Mahindra	4.2
17.	Stanly Alexander Mathews	Mahindra & Mahindra	4.2
18.	Vaibhav Pandurang Gawai	Mahindra & Mahindra	4.2
19.	Akshada Anil Ghaduse	Capgemini	4.25
20.	Apeksha vilas warude	Capgemini	4.25
21.	Gauri Balasaheb Gangurde	Capgemini	4.25
22.	Harshali Mahadeo Chikankar	Capgemini	4.25

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Training and Placement Cell (Campus Placement)

Sr. No.	Name of Student	Company Name	Package
23.	Payal Rajendra Bhamre	Capgemini	4.25
24.	Priyanka Pradip Dusane	Capgemini	4.25
25.	Tanvi Shivajirao Paul	Capgemini	4.25

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Certificate of Participation

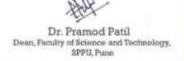
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has Participated and successfully completed One Day Workshop on "National Education Policy (NEP) 2020" organized by Sanjivani College of Engineering, Kopargaon in association with Savitribai Phule Pune University, Pune on 11/05/2024.









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